

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicants:** Wing K Luk, et al.

**Examiner:** Bernstein, Allison

**Serial No.:** 10/735,061

**Group:** Art Unit 2824

**Filed:** December 11, 2003

**Docket:** YOR920030136US1 (8728-621)

**For:           GATED DIODE MEMORY CELLS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

**AMENDMENT**

This is a response to the Office Action mailed on October 16, 2006. Please amend the claims as follows: